**Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ ID: \_\_\_\_\_\_\_\_\_\_\_\_\_**

**Department of Computer Science and Engineering**

**CSE340: Computer Architecture   
Spring 2014**

**Quiz-2**

**Full Marks: 15 Time: 25 Mins**

1. What do you mean by zero and sign extended? **2**
2. **1 00001111 101010000000000000000000** convert this to decimal value using **IEEE754 32-bit** floating point representation. **3**
3. Write Show the **IEEE 754** binary representation of the floating point number **-11.1** using eight bit register have one bit for sign, three bits for exponent and four bits for fraction part. **5**

1. Add **0.5ten** and **-0.4365ten** IEEE 754 floating point addition. **5**